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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Amos Intrater et al.

Appln. No.: 09/234,427

Filed: January 20, 1999

For: INTEGRATED DIGITAL SIGNAL
PROCESSOR/GENERAL PURPOSE CPU
WITH SHARED INTERNAL MEMORY

Group Art Unit: 2183

Examiner: D. Pan

SUPPLEMENTAL AMENDMENT (TO
OFFICE ACTION DATED MAY 31, 2002)

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the
United States Postal Service, postage prepaid, in an envelope,
addressed to Box Non-Rec-Amt, Commissioner for Patents,
Washington D.C. 20231-9999 on August 28, 2002.

Dated: 08-28-02 By: Julian L. Keay

Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

In response to the Official Action mailed May 31, 2002, please amend the above-
identified application as follows:

Received

In the Claims

Please cancel claims 11-17, 19-26, 28-35, and 40-44.

FEB 03 2003

Technology Center 2100

The claims have been amended to read as follows:

18. (Amended) A data processing system comprising:
a first bus;
a memory connected to the first bus;
a general purpose processor (GPP) connected to the first bus, the GPP loading
operands into the memory; and
a digital signal processor (DSP) connected to the first bus, the DSP having a register
and starting execution of an instruction in response to the GPP loading information into the
register, the DSP only executing a single instruction when said information is loaded into the
register.